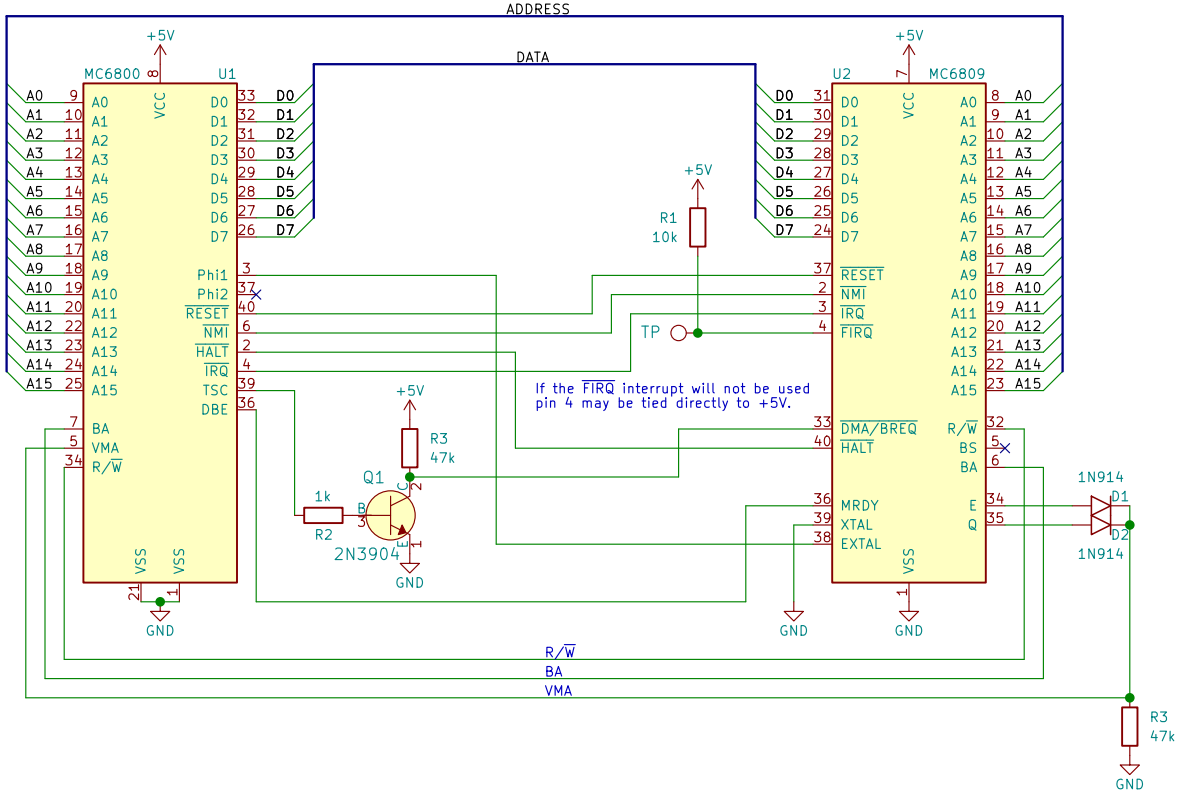

NOTES:
Part values are not critical. Any general purpose NPN transistor and silicon diodes can be used. Any value of resistor from 1k to 47k for the pull-ups or pull-downs will work. The only exception is R2. You don't want too low a value for R2 but it must be low enough to allow Q1 to operate at or near its saturation point to ensure that DMA/BREQ (pin 33 of U2) is pulled down to a logic zero level when TSC (pin 39 of U1) goes high.

Kevin Cozens

Sheet: /

File: Adapter-6800to6809.sch

Title: 6800 to 6809 adapter board

Size: USLetter	Date: December 20, 2018	**Rev: 1**
KiCad E.D.A. eeschema (6.0.0-rc1-dev-1527-gdecc7ed-dirty)		Id: 1/1